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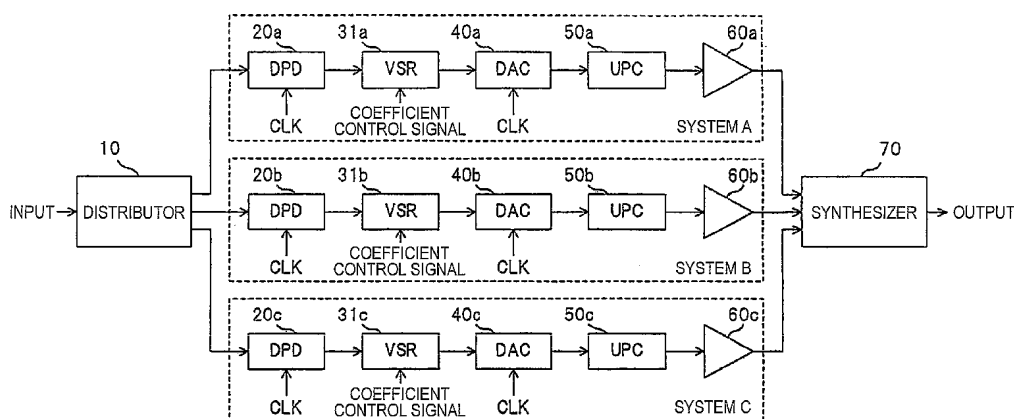
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(54) Title: POWER AMPLIFYING APPARATUS, POWER COMBINING SYSTEM AND DELAY MEASURING METHOD FOR POWER COMBINING SYSTEM



(57) Abstract: A power amplifying apparatus includes a distributing unit (10) that divides an input digital signal to a plurality of the input digital signals so as to distribute the input digital signals to a plurality of devices respectively, and a synthesizing unit (70) that synthesizing output signals from the devices to output the synthesized output signal. Each of the devices includes a delay regulating unit (20a, 20b, 20c, 31a, 31b, 31c) that regulates a delay amount of the input digital signal, a digital/analog converting unit (40a, 40b, 40c) that converts the digital signal regulated by the delay regulating unit (20a, 20b, 20c, 31a, 31b, 31c) to an analog signal, and an amplifying unit (60a, 60b, 60c) that amplifies the analog signal to output the amplified analog signal to the synthesizing unit (70).

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